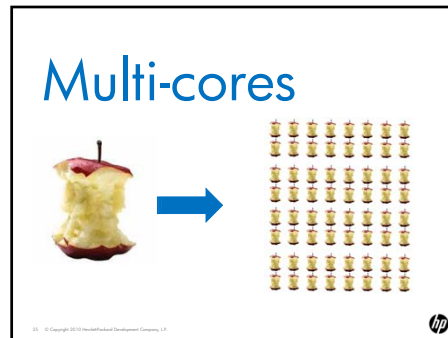


MIPS per Watt

MIPS per What?

Another key challenge...

scale



5 global data centers
 X 40 containers per dc
 X 10 racks per container
 X 4 enclosures per rack
 X 16 blades per enclosure
 X 2 sockets per blade
 X 32 cores per socket
 X 10 VMs per core

81,920,000

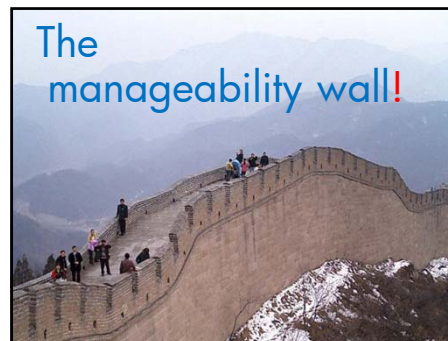
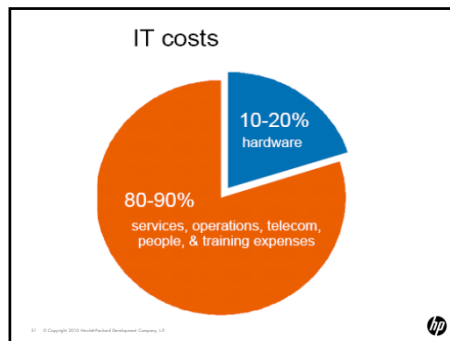
Virtual servers

Bring-up
 Operations
 Diagnostics

Tuning
 Retirement

Huge problem

Large fraction of total IT costs



Also...

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Reliability challenges

Dead horses & killer sharks!

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One-tenth total costs!

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*We have a competitive advantage because we have the **cheapest** and most scalable architecture.*

Eric Schmidt, Google

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1. 1000X Performance @ exascale
2. Low power and sustainability
3. Scalable coordinated management
4. Reliability from increasingly unreliable components
5. Low costs and innovation over volume components

Interesting opportunities

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How can we address **all** these challenges?

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Will need
cross-disciplinary
co-design

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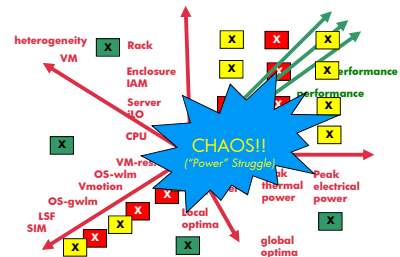
3 examples

"No power struggles"
Dematerialized datacenters
Nanostores

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Power Struggles!



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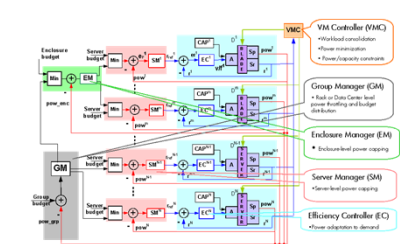
Coordinated
management

Cross-disciplinary
collaboration across...
systems, architecture,
power & cooling,
control theory

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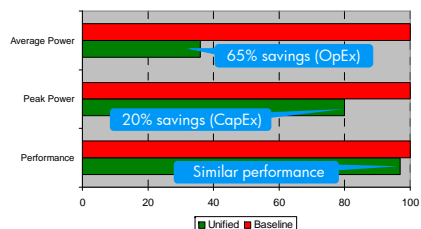
Coordinated architecture



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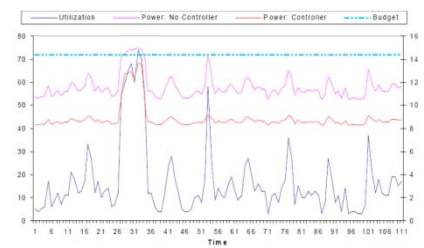


It works well!



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It works well!



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But...
more
opportunities...

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Interfaces?
Extensibility?
Federation?
Scale?

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Cross-disciplinary
collaboration across...
computer engg,
environmental engg,
mechanical engg...

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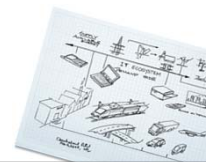
Supply vs demand side
energy consumption

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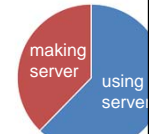
Energy in extraction,
transport, manufacturing,
reclamation...

Energy in operation



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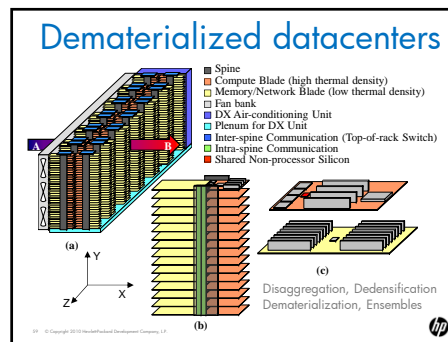
Exergy to
capture
destruction of
resources



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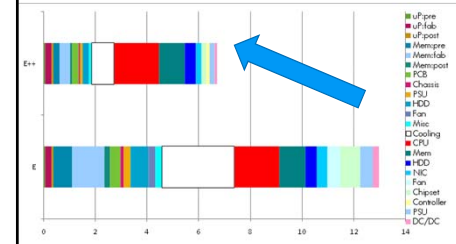
Revisit systems
management/design
for total lifecycle
exergy...

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2X better exergy!



2X better exergy
...over aggressive
energy-optimized
baseline

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New tradeoffs for
optimization...
New opportunities

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Cross-disciplinary
collaboration across...
device physics, computer
engineering, systems
software...

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Cloud 2018 will
be about data

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5 exabytes in 2002 to 280 exabytes in 2009 of online data
"Physics of data, Myers, Google"

56X
in seven years

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5 exabytes in 2002 to 280 exabytes in 2009 of online data
"Physics of data, Myers, Google"

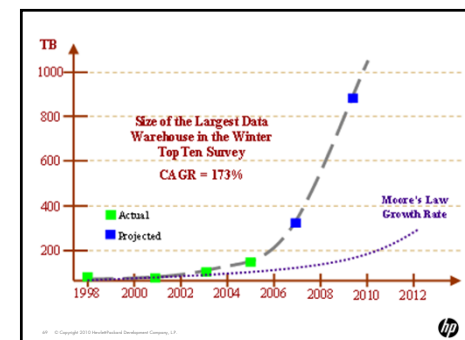
56X
in seven years

16X
Moore's law

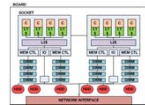
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Data on faster
exponential vs
compute

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Hierarchy helps
data-centric
performance
but...



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Exergy overhead

Materials
Power



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Technology
trends enable
new designs...



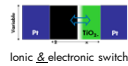
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Scientists Create First Memristor: Missing Fourth Electronic Circuit Element

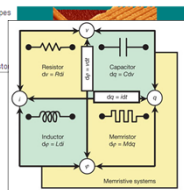
By Bryan Gaser 4 April 2008 | 10:03 am | Categories: Uncategorized

Researchers at HP Labs have built the first working prototypes of an important new electronic component that may lead to instant-on PCs as well as analog computers that process information the way the human brain does.

Memristor is called a memristor, or memory resistor.



ionic & electronic switching



Memristive systems

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Simpler processors at
near-threshold voltage for
higher energy efficiency



Photonics for improved
bandwidth, energy
efficiency, high radix...



3D-stacked Memristors for non-volatile storage?

Diode-less architecture (4F²). Stack multiple layers, Materials integration w/ CMOS

Technology	Density ($\mu\text{m}^2/\text{bit}$)	Bandwidth (GB/s)	Latency Read (ns)	Latency Write (ns)	Energy Read (pJ/b)	Energy Write (pJ/b)
Hard Disk	N/A	0.5	3,000,000	3,000,000	2500	2500
Flash SSD [3] [6]	0.0021	1.0	25,000	200,000	250	250
DRAM [6] [20]	0.0038	51.2	55	55	24	24
PCRAM (22nm) [30]	0.0048	variable	48	150	2	19.2
Memristor (22nm) [8]	0.0048	variable	100	100	1-3	1-3

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What would you do with

TBytes

nonvolatile storage on-chip?



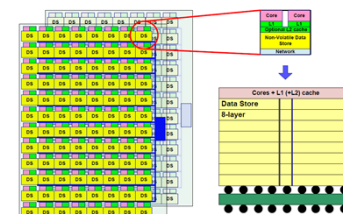
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Discontinuity in
assumptions that
led to traditional
system designs?



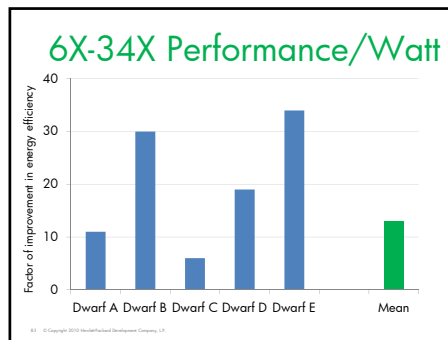
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From μ processors to
nanostores...



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New designs...

New opportunities

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Just a few examples...

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Closing...

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What does all of this mean for you?

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