



Commercializing Auditory Neuroscience

Lloyd Watts
Audience, Inc.

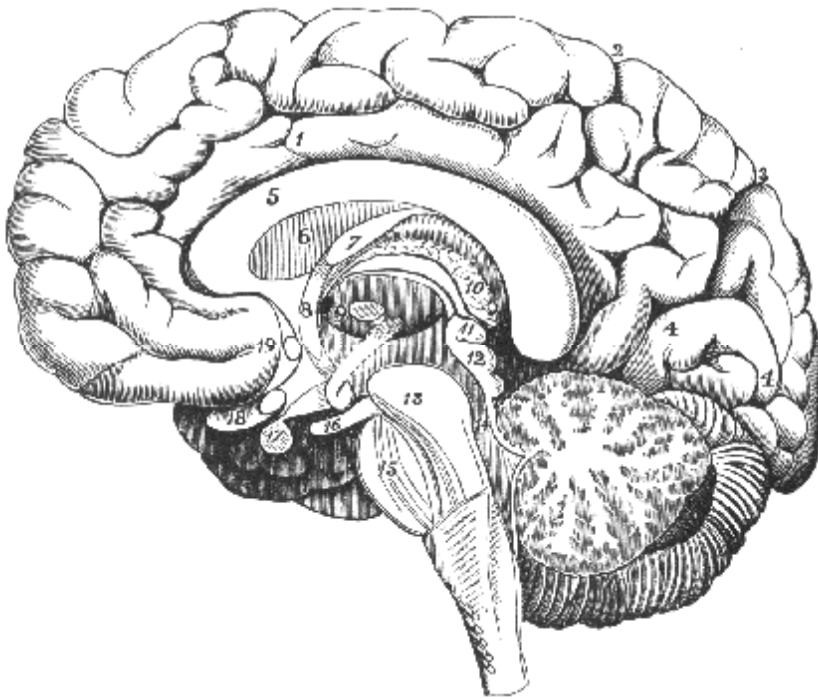
U.S. Frontiers of Engineering Symposium
Dearborn, Michigan
September 21-23, 2006

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Overview

- Can we build a machine that hears the way human beings do?
 - q Original passion: music transcription
 - q Reverse-engineer the auditory pathway, based on neuroscience
 - q Do we know enough about the brain? Are computers capable enough?
- If so, can we build a commercially successful company out of it?
 - q Can we raise the money (i.e., convince the investors)?
 - q What application to shoot for?
 - n Music Transcription? No...
 - n Speech recognition? No...
 - n Noise suppression for Cell-phones? Yes!
 - q Building a team, really executing
 - q Is it a chip company, or a software company?
 - q Investors, Customers, Employees, Advisors all have to see short-term progress, and long-term return

Do we know enough about the brain to build one?



(Gray's Anatomy, 1901) (Moravec, 1998)
(Kozyrakis et al., 2001)

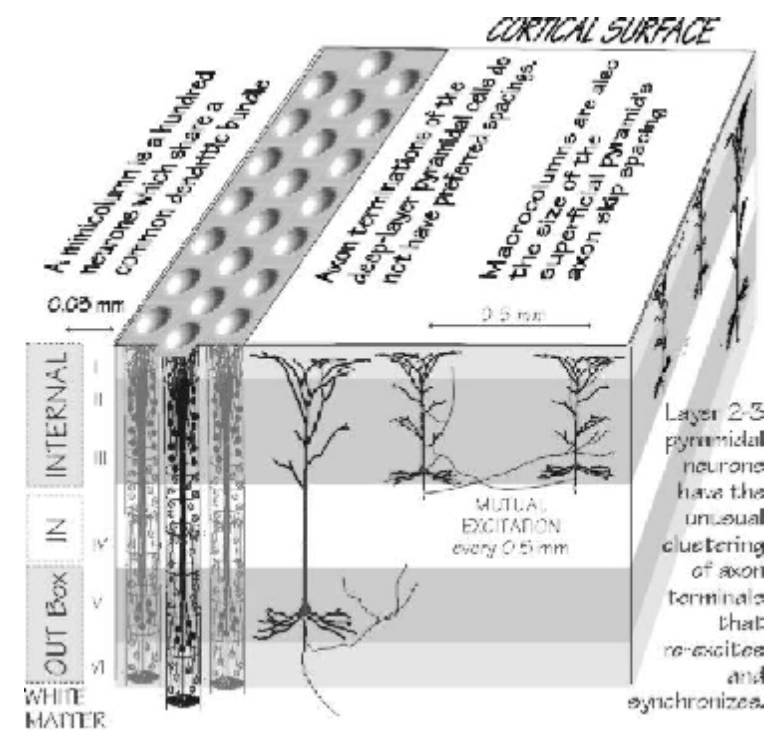
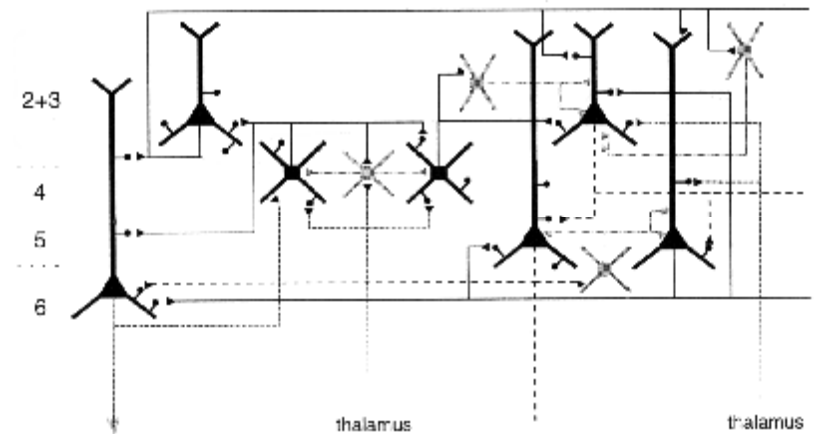
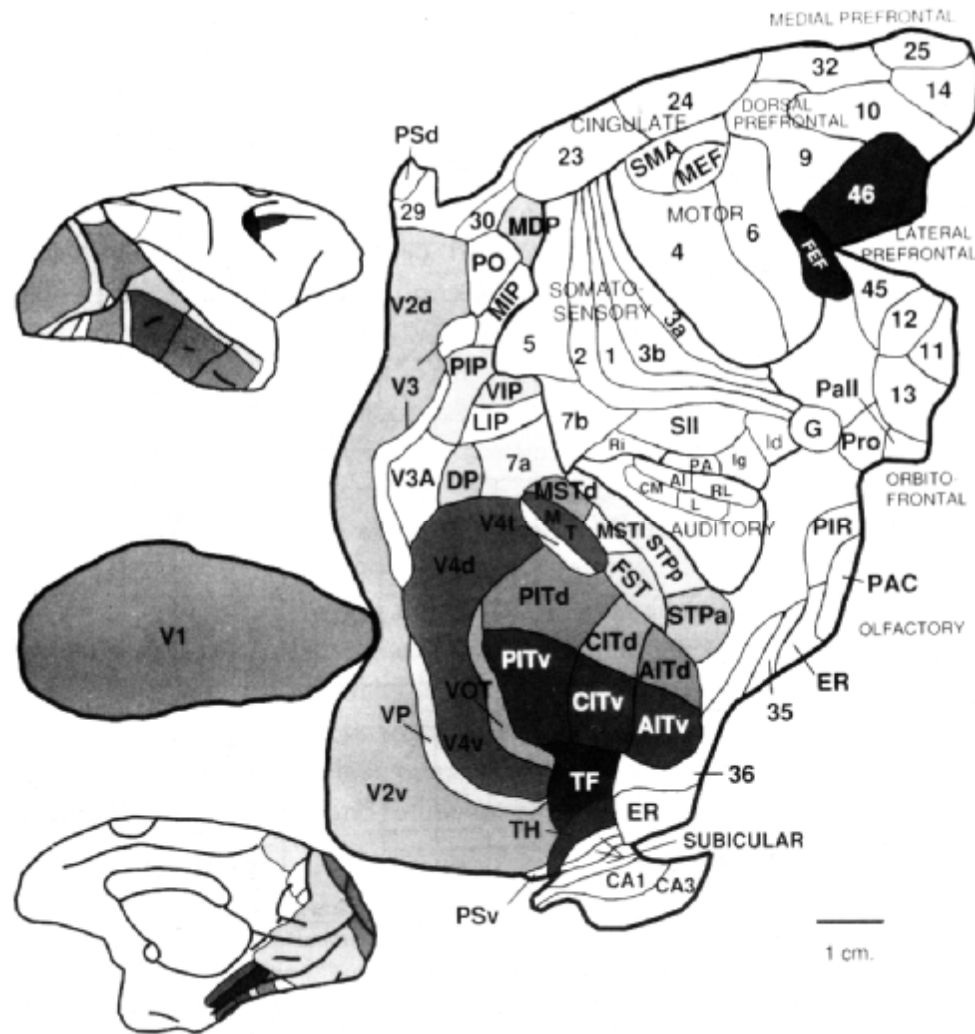
- q $\sim 10^{11}$ neurons, $\sim 10^{14}$ synapses
- q $\sim 10^{16}$ Ops/s, $\sim 10^{14}$ MByte
- q ~ 20 W power consumption
- q $\sim 10^6$ GOPs/W efficiency (compared with ~ 3 GOPs/W for current HW)
- q $V_{dd}(\text{brain}) = 80\text{mV}$ accounts for nearly 3 orders of magnitude of power efficiency, trades power consumption against area/cost/yield $P = CV^2f$
- q Vast variety of cell types
- q Thousands of modules/regions

being studied by

- q $\sim 30,000$ neuroscientists
- q **No individual knows everything.**

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Dramatic increase in knowledge in last 20 years...

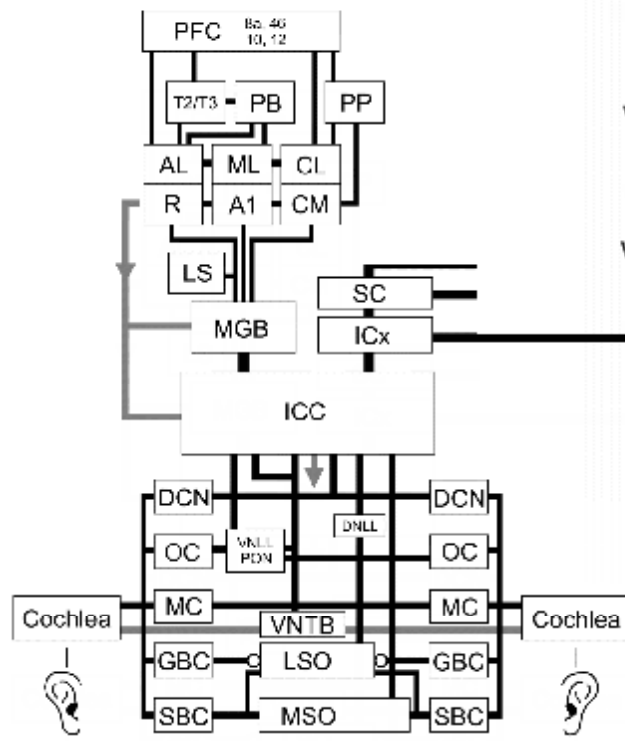


(van Essen and Anderson, 1990)

(Douglas and Martin, 1998), (Calvin, 1996)

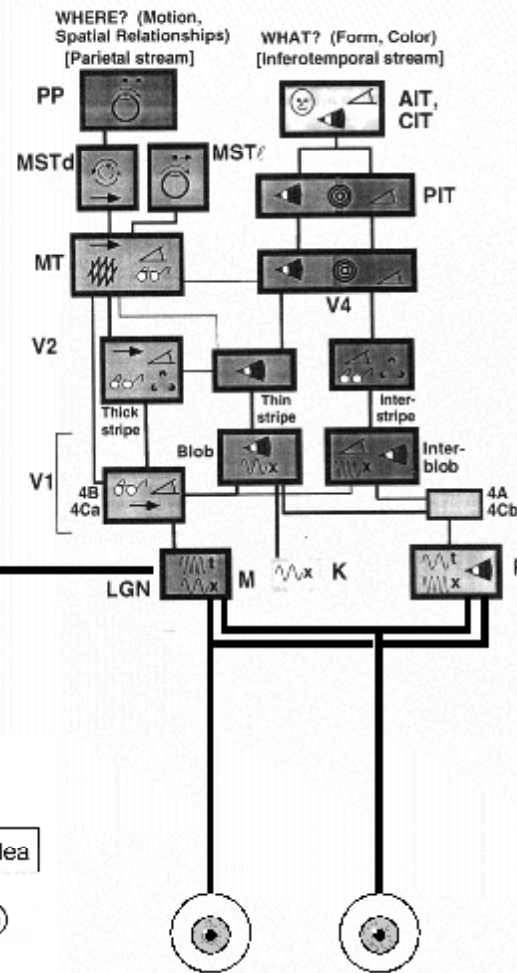
Collectively, do we know enough about the brain to *begin* building a realistic model?

Auditory



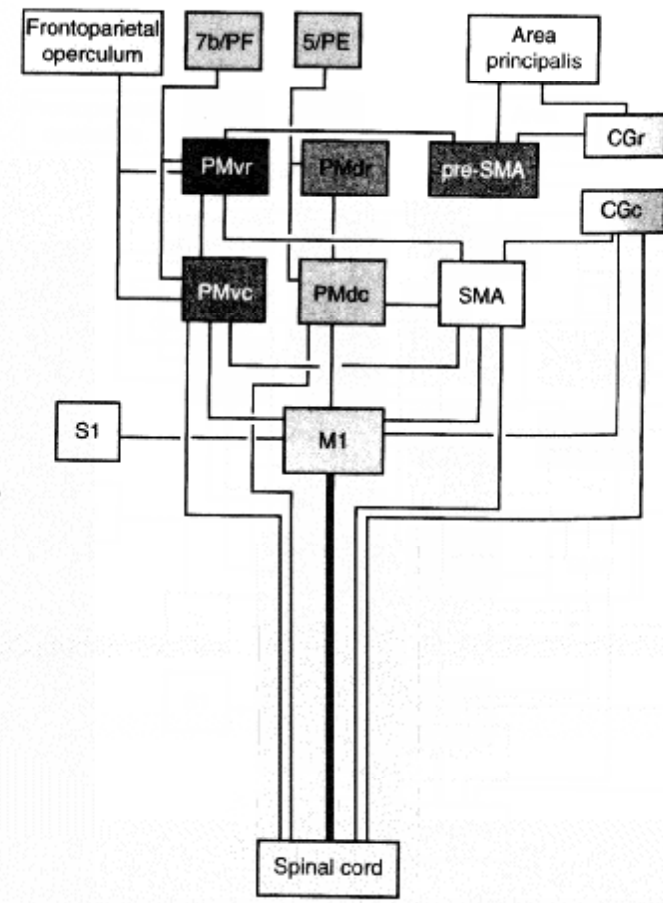
(Kiang, Oertel, Covey, Rauschecker)

Visual



(van Essen and Gallant, 1994)

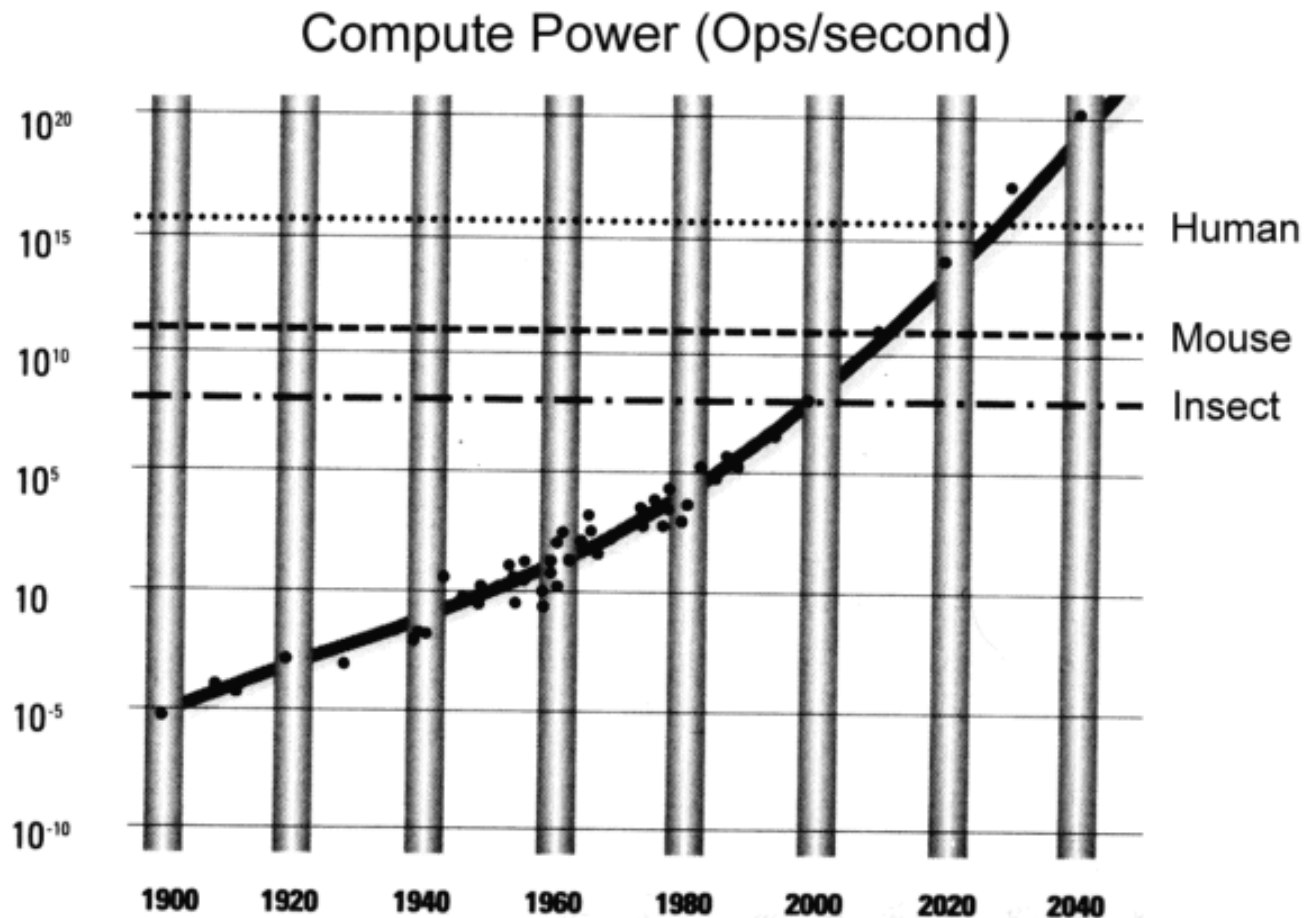
Motor



(Zigmond et al., 1999)

etc...

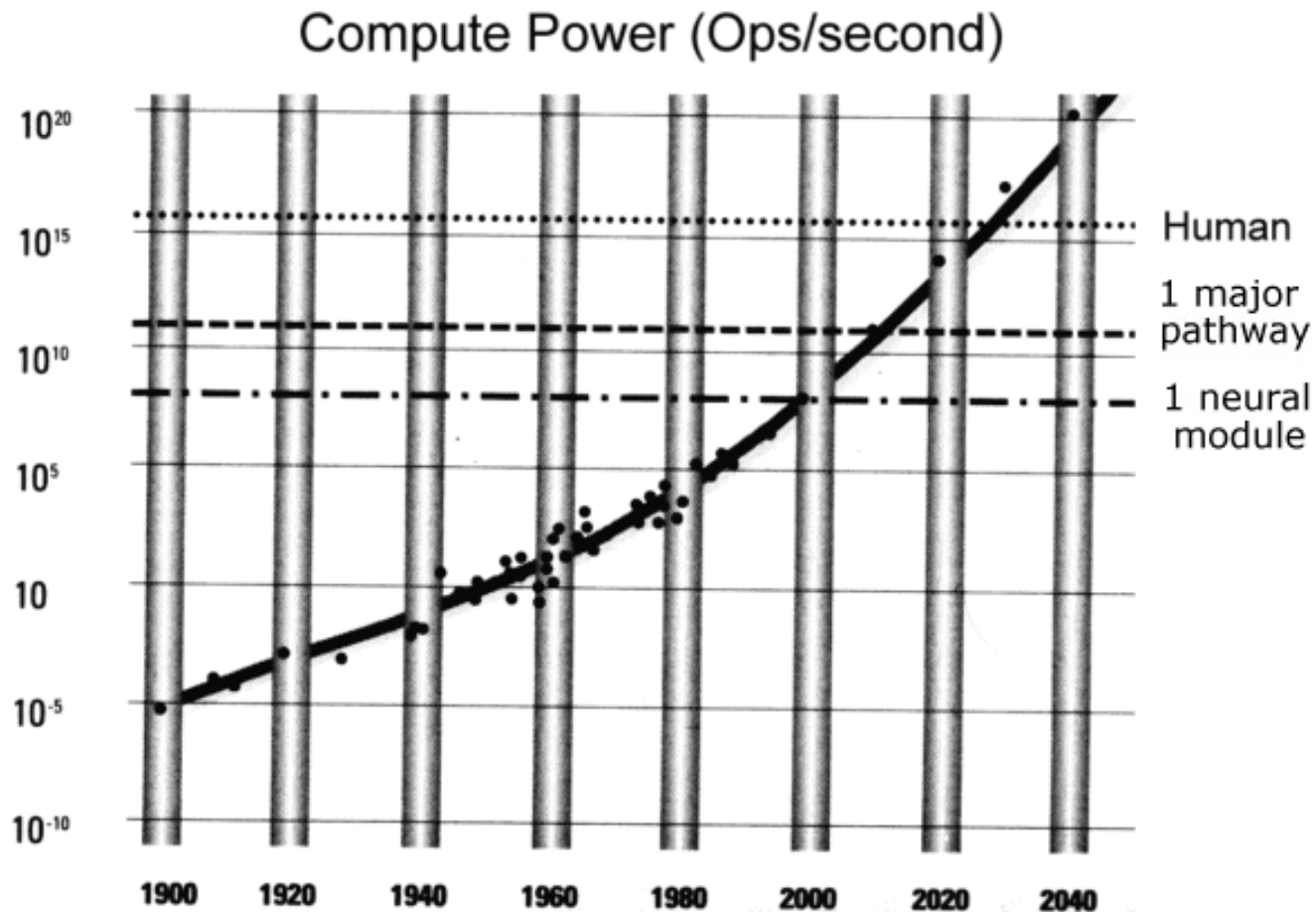
When will computers be capable enough?



(Ray Kurzweil, *The Age of Spiritual Machines*, 1999)

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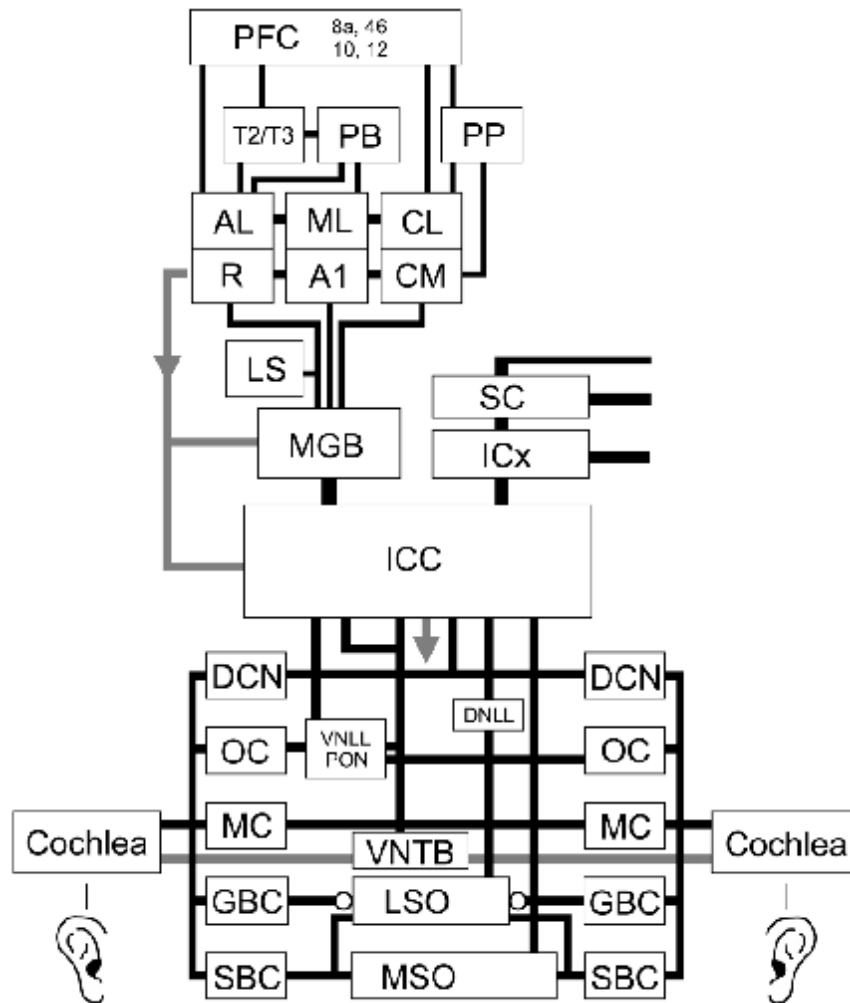
When will computers be capable enough?



Are there fundable, commercially viable applications for the major pathways (audition, vision, motor)?

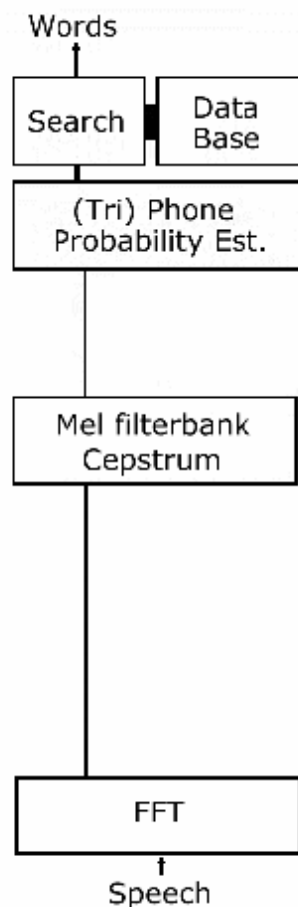
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Auditory Pathway

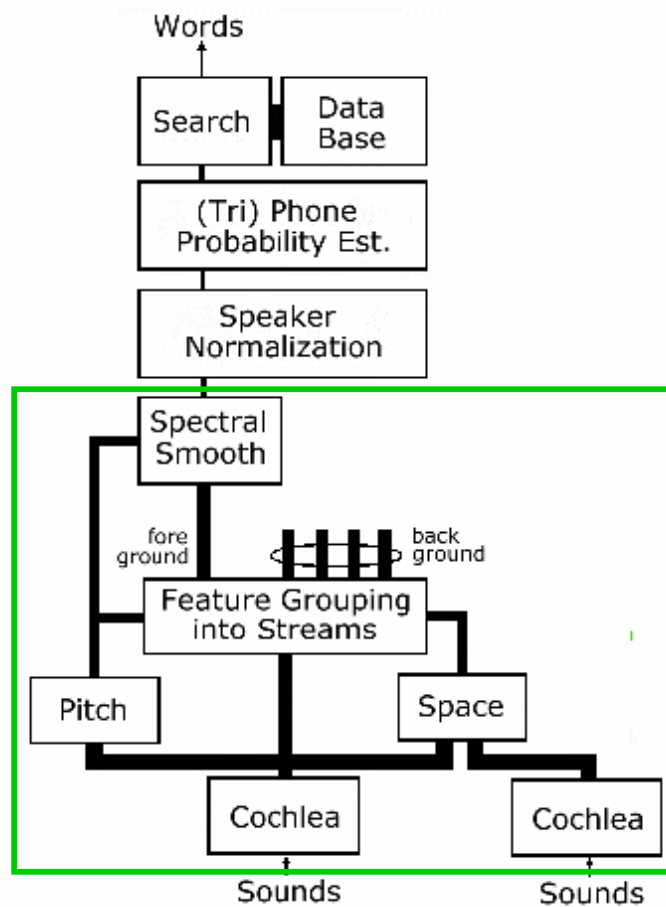


- Cortical functions: extensive pattern match, hypothesis generation and pruning, object tracking, HMM/Viterbi search, associative memory
- High-res feature detection, cross- and auto-correlation, and post-processing
- High-resolution sensory pre-processing

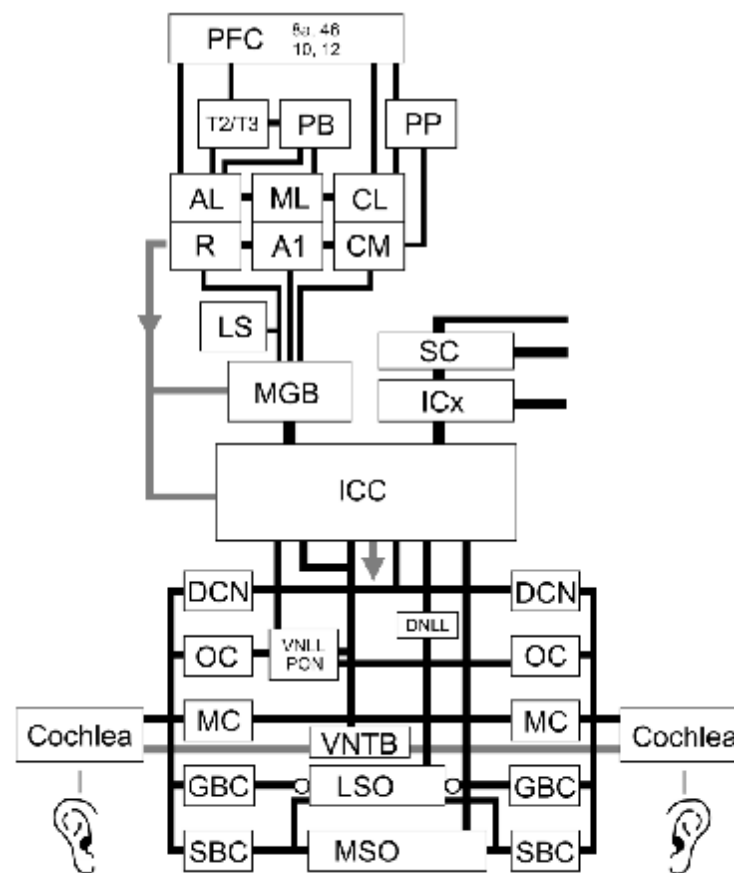
Commercial Example: Stream Separation for Speech Recognition and Telecom



Single-source
Recognition



Multi-source
Separation and Recognition



Real-Time Demos

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Audience Introduction

- q Fabless semiconductor and embedded software company with offices in Mountain View, CA
- q Series B closed in April '06, \$15M – total \$25M to date
- q First to market with commercial grade noise suppression based on the human hearing system
- q Provides dramatic & reliable suppression of highly non-stationary (fast changing) noise sources such as another voice, music or ringtones
 - q 20dB to 25dB noise suppression (non-stationary & stationary noise)
- q Prominent investors & board members
 - q New Enterprise Associates
 - q Tallwood Venture Capital
 - q Vulcan Capital
 - q Carver Mead

Audience Background

q Management

- q Peter Santos – President & CEO, Director
 - n LSI Logic, Voyan, Barcelona
- q Lloyd Watts, PhD. - Founder, Chairman & CTO
 - n Interval Research, Synaptics, Arithmos
- q Bill Hoppin – VP Sales and Business Development
 - n Celeritek, Accelerant Networks, Synopsys

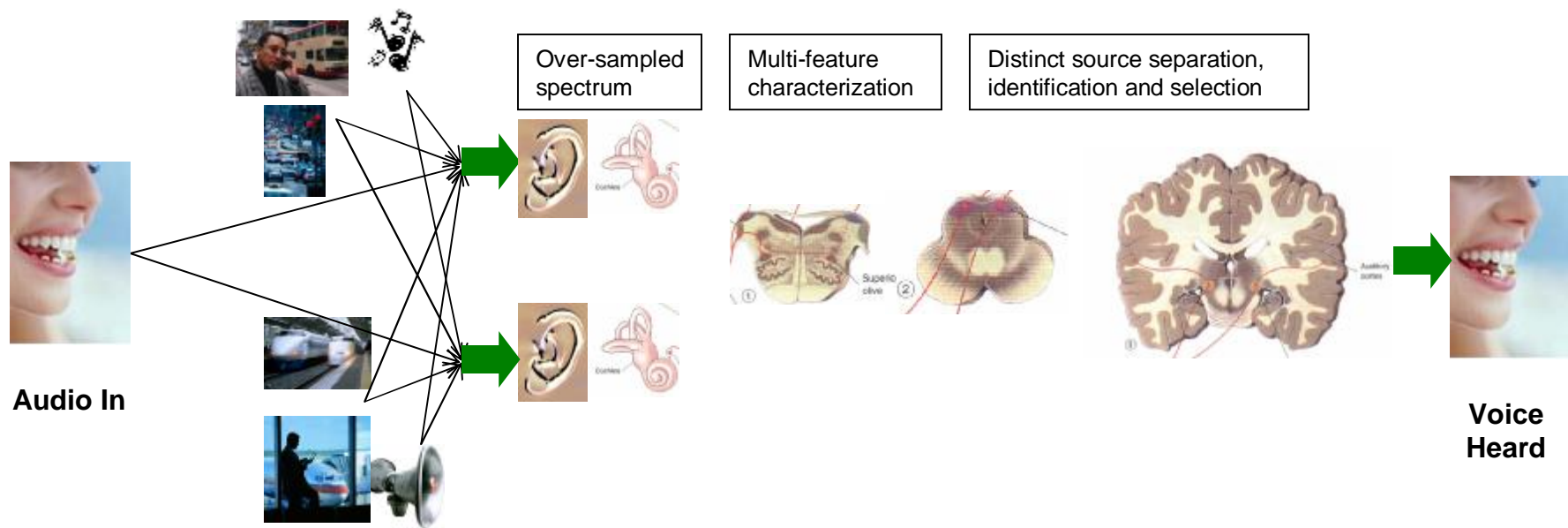
q Outside Directors/Observers

- | | |
|---|----------|
| q Forest Baskett, New Enterprise Associates | Director |
| q George Pavlov, Tallwood Venture Capital | Director |
| q Steve Hall, Vulcan Capital | Director |
| q Carver Mead | Director |
| q Patrick Chang, TSMC/VentureTech | Observer |

q Advisors

- q Dr. Lawrence Rabiner (Rutgers, ex-AT&T Bell Labs)
- q Dr. Robert Colwell (ex-Intel Pentium architect)
- q Dr. Vladimir Cuperman (UCSB)
- q Dr. Hynek Hermansky (OGI)
- q Dr. Abeer Alwan (UCLA)
- q Ray Kurzweil

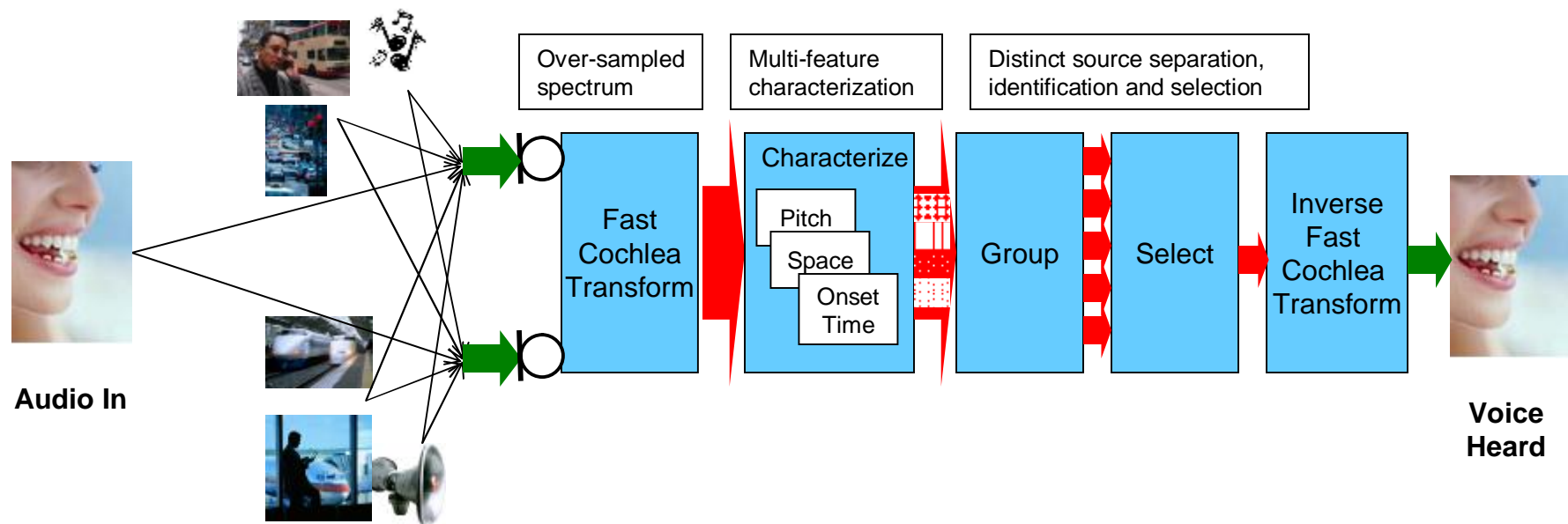
The Biological System



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The Audience System

Near Complete Suppression of Difficult, Distracting Non-stationary Noise
(as well as Stationary Noise)



Slowly Changing *Stationary* Noise

Fans, Crowds, Wind

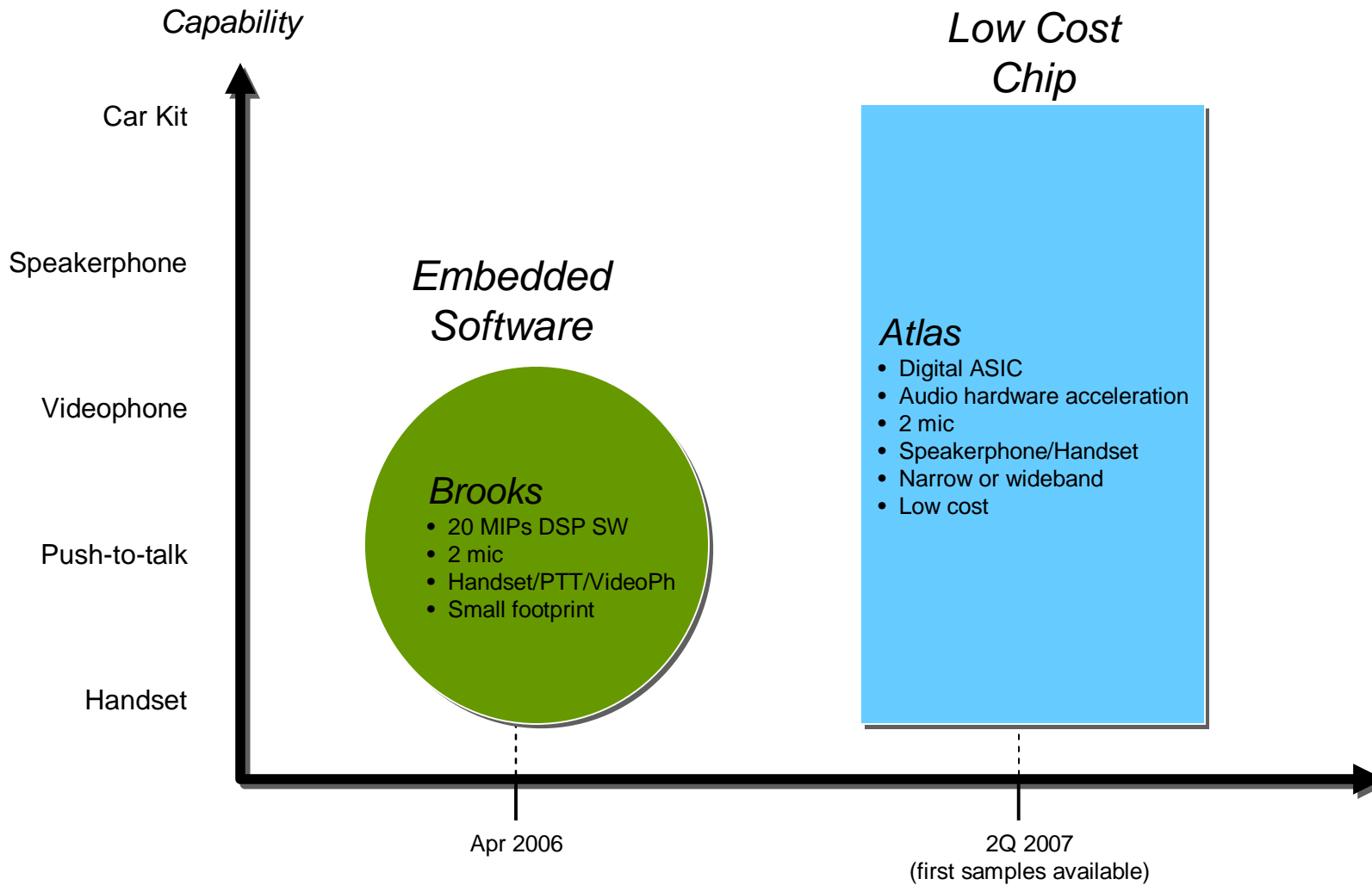
Fast Changing *Non-Stationary* Noise

Sirens, voices, music

PA systems, trains

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Audience Product Roadmap

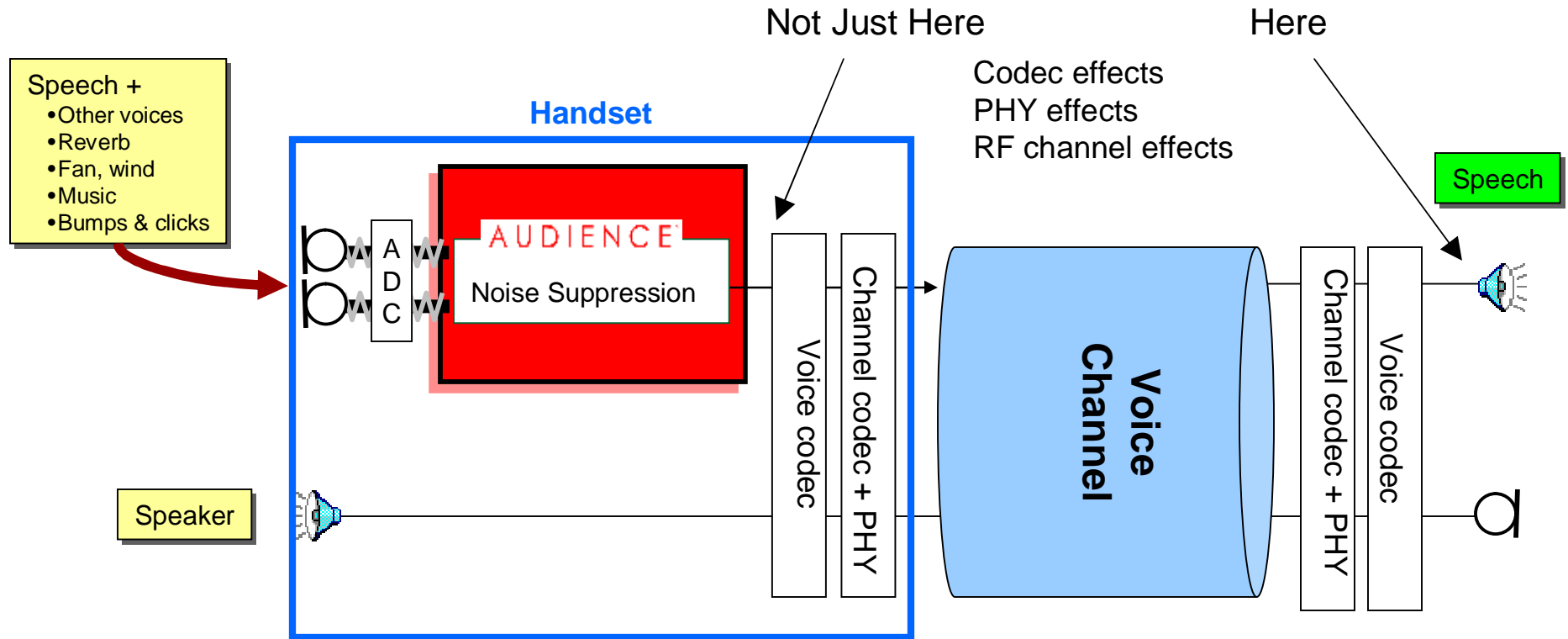


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Audience Voice Quality Enhancement in Mobile Terminal

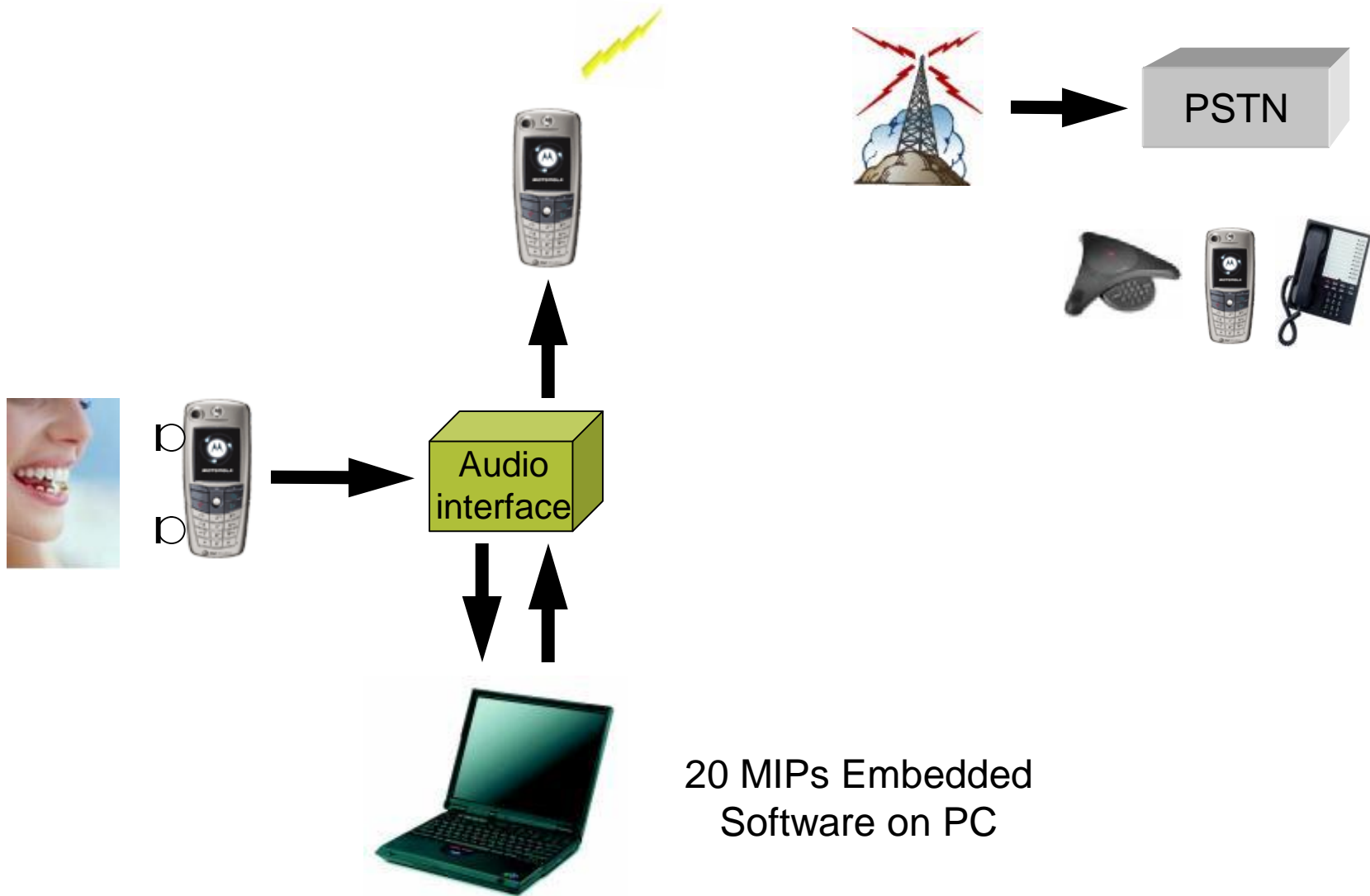
Audience system level optimization

- 1.) Aligned to specific terminal acoustics
- 2.) Optimized for performance where it matters: in the system



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Audience real-time demonstration setup



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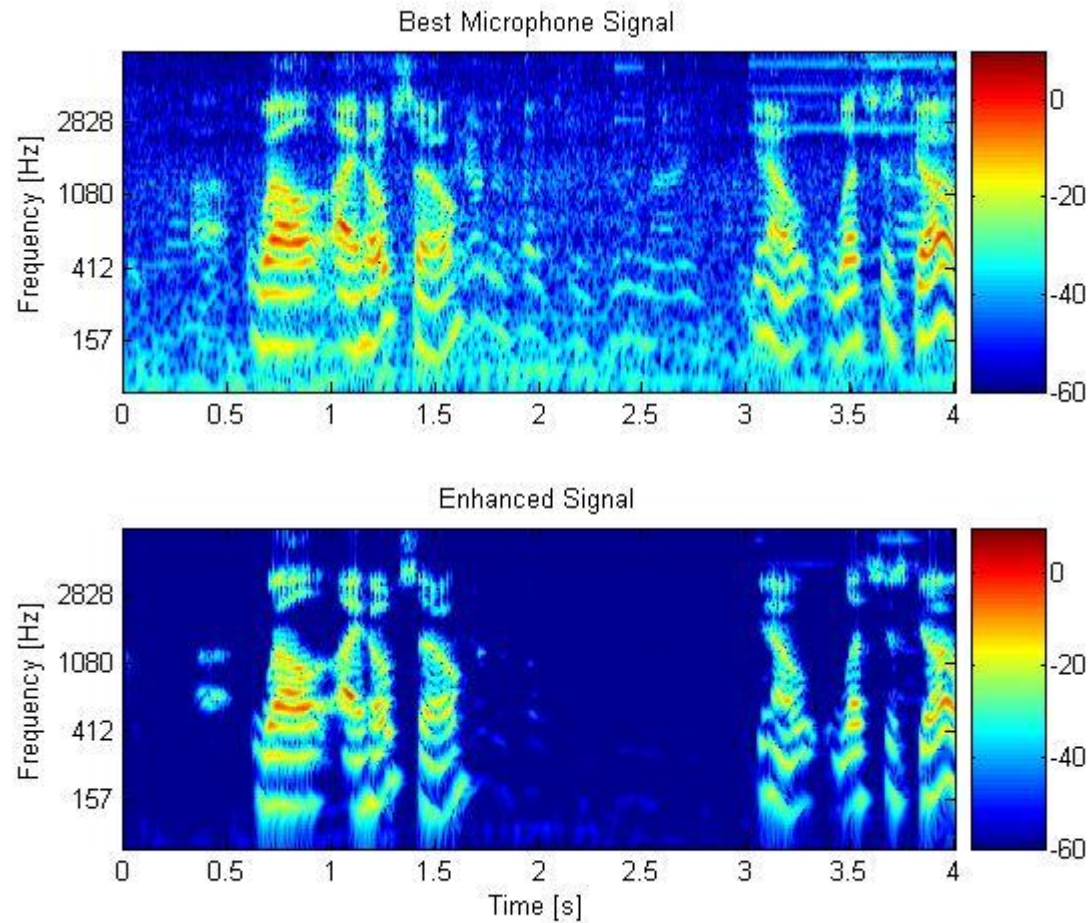
Audience real-time demonstration setup



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Handset demonstration Streetnoise

Brooks
Near Mic
Embedded SW



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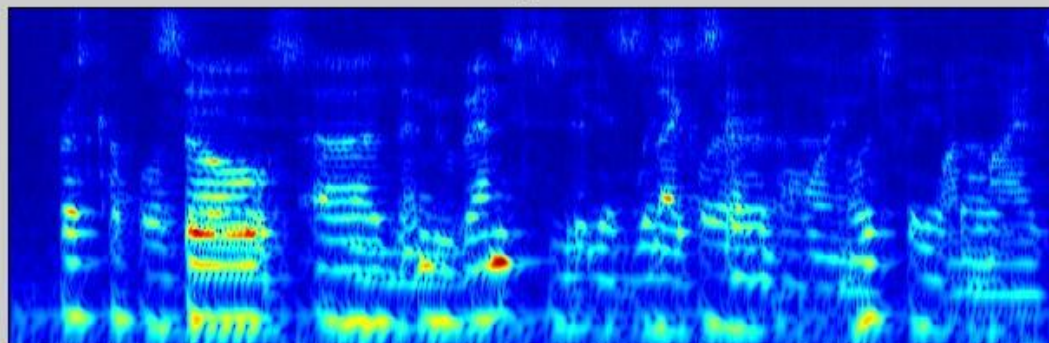
Speakerphone demonstration Competing voice

Atlas
Far Mic
ASIC



Log
frequency

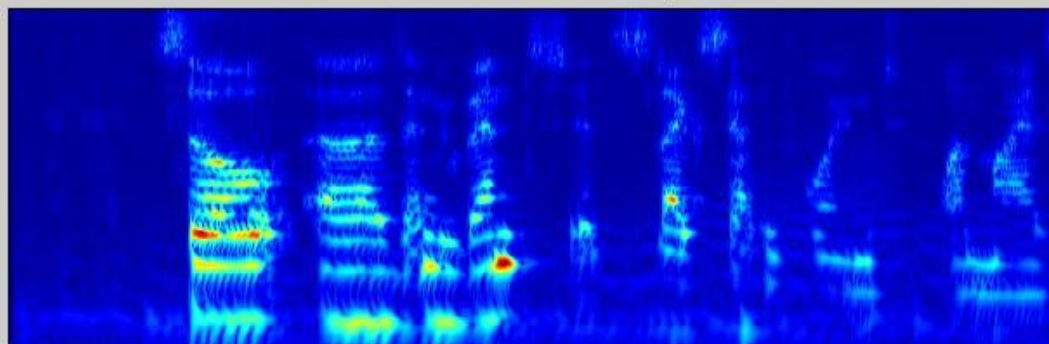
Original



Time

After Processing

Log
frequency



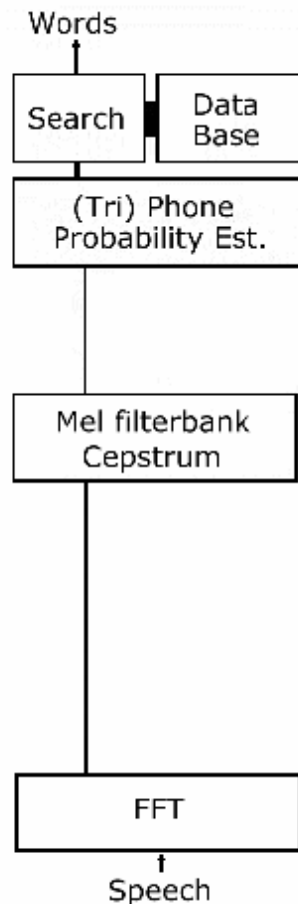
Time

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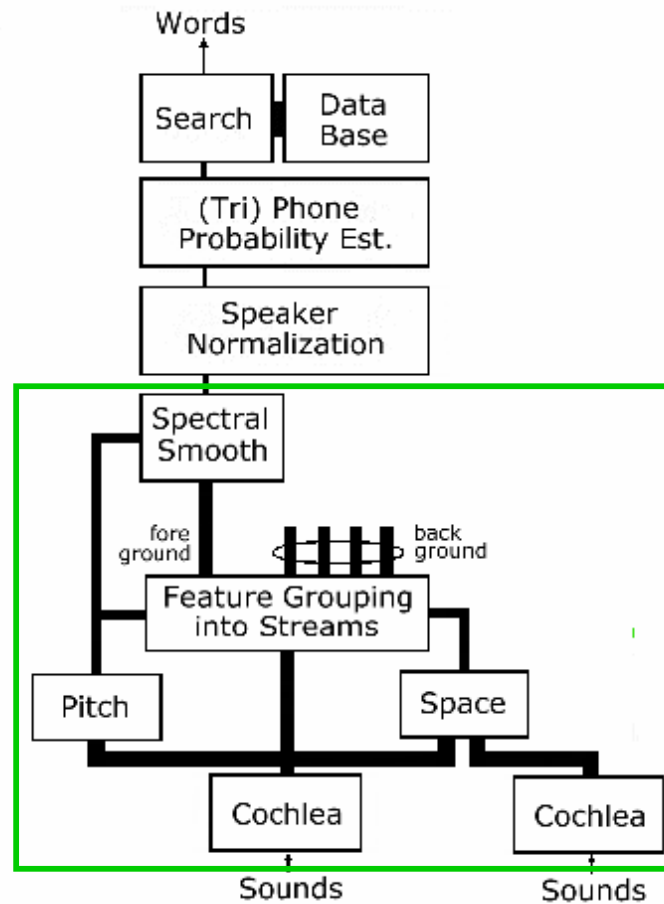
Recap and Conclusions

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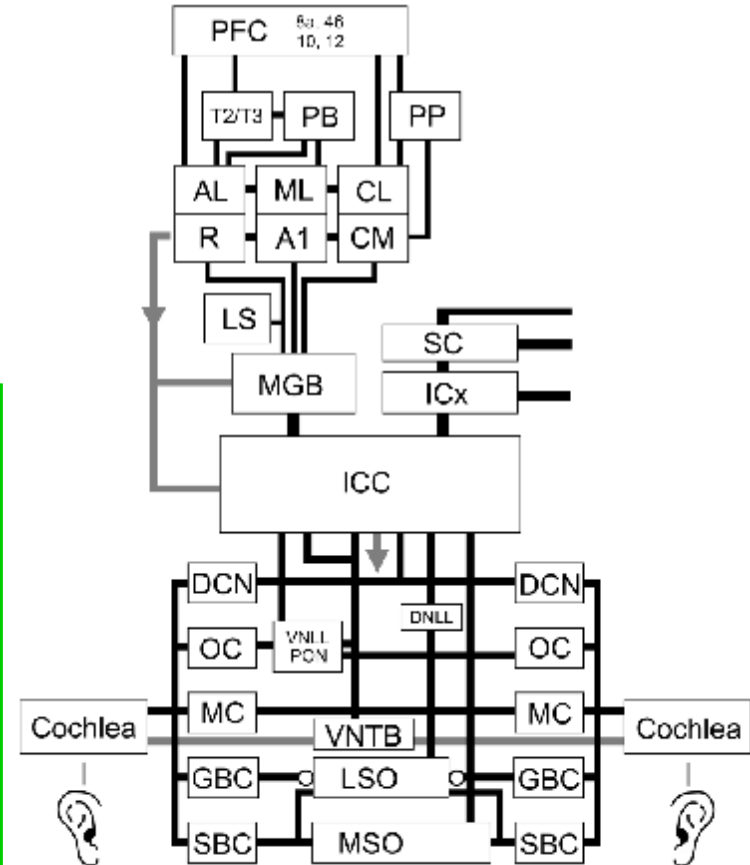
Commercial Example: Noise Robustness for Speech Recognition and Telecom



Single-source
Recognition

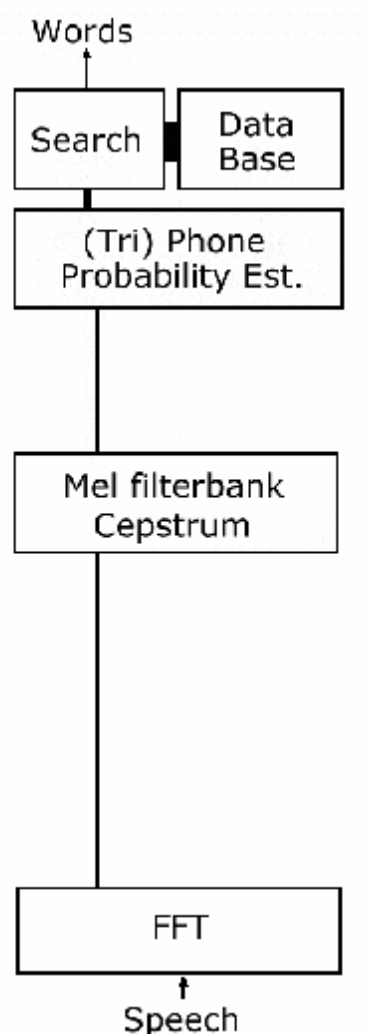


Multi-source
Separation and Recognition



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Resolution Requirements are different for Multi-Source Separation than Single-Source Recognition



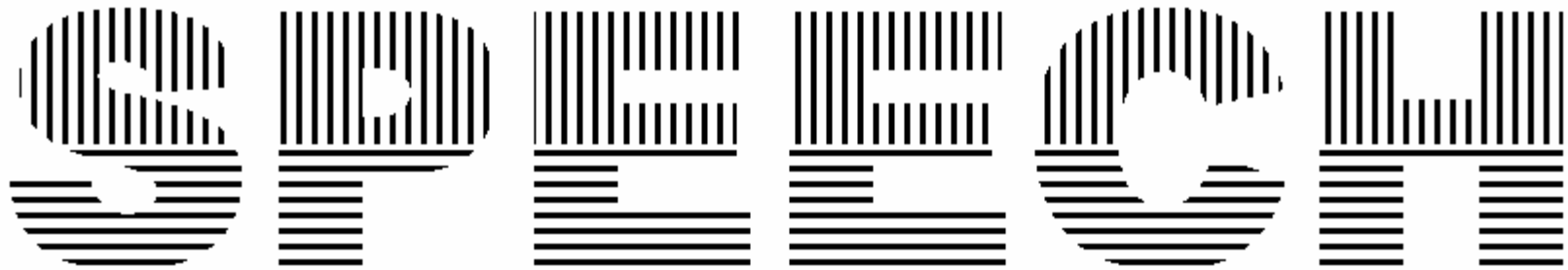
"The selection of the best parametric representation of acoustic data is an important task in the design of any speech recognition system. The usual objectives in selecting a representation are to compress the speech data by eliminating information not pertinent to the phonetic analysis of the data and to enhance those aspects of the signal that contribute significantly to the detection of phonetic differences... Compact storage of the information [is] an important practical consideration."

-- Paul Mermelstein (inventor of mel-frequency cepstral coefficient), 1980.

"The very purpose of phonetic classification in ASR is a significant reduction of the information carried by the speech signal. Thus, the front end processing should be supportive of this task."

-- Hynek Hermansky, 1998.

A Visual Analogy



- q Voiced speech contains fine structure due to pitch, speaker ID...

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A Visual Analogy

SPEECH

- q Voiced speech contains fine structure due to pitch, speaker ID...
- q Blurring eliminates fine structure, making pattern matching easier...

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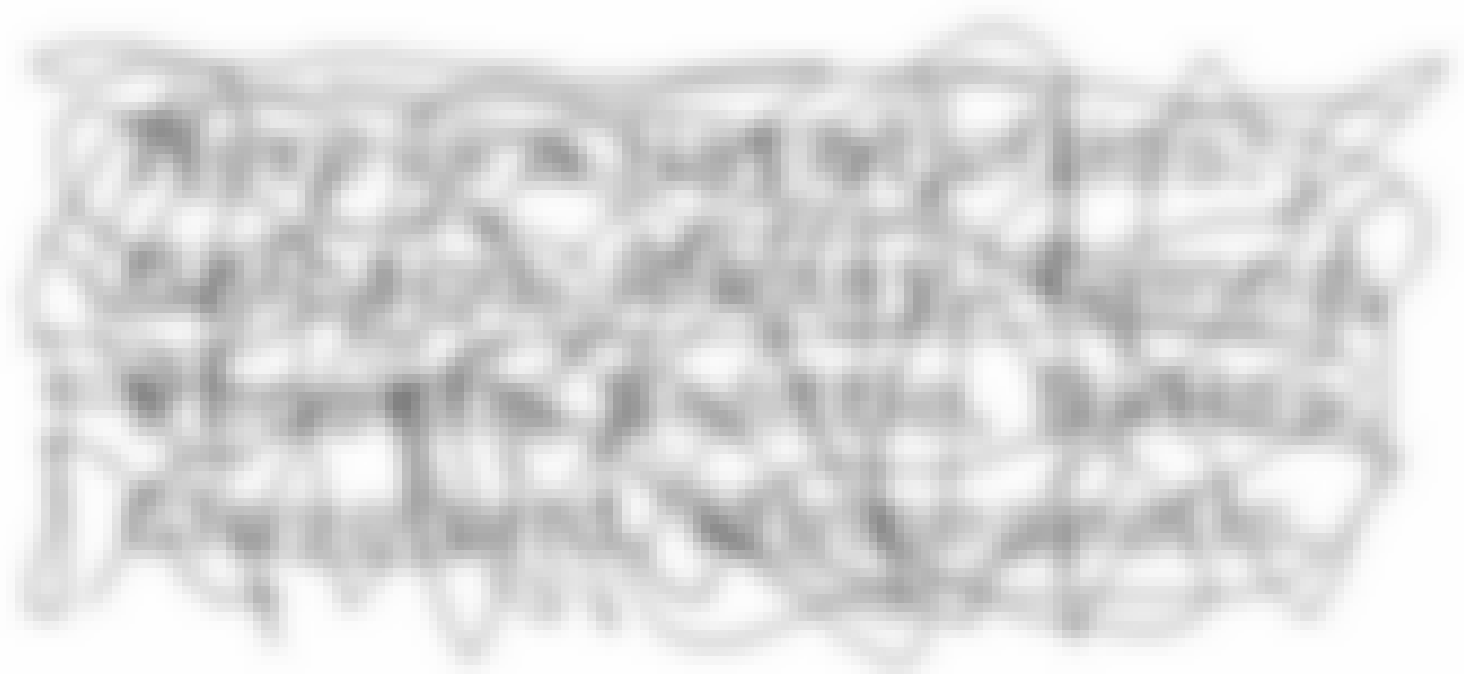
A Visual Analogy

SPEECH

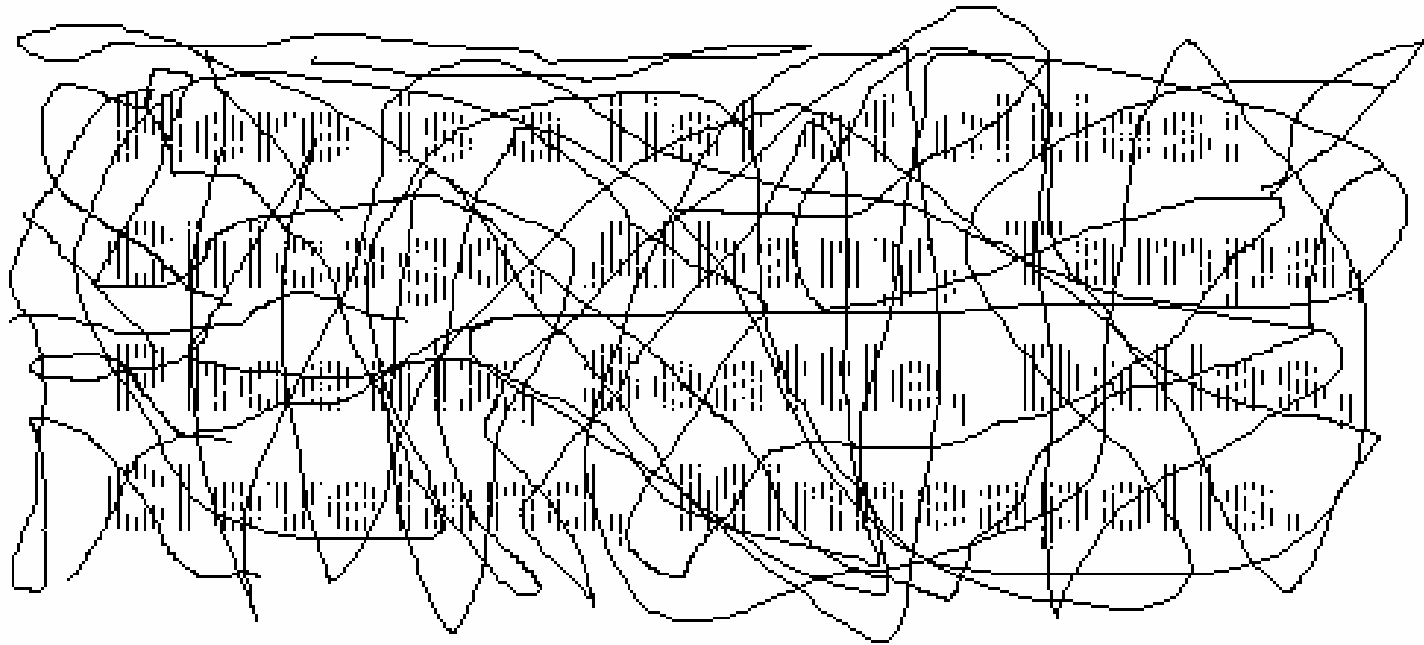
- q Voiced speech contains fine structure due to pitch, speaker ID...
- q Blurring eliminates fine structure, making pattern matching easier...
- q And justifies downsampling, which reduces downstream system cost.

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Here is a list of states:
Alaska, Ohio, Florida,
Nebraska, Delaware,
Kansas, Wisconsin.



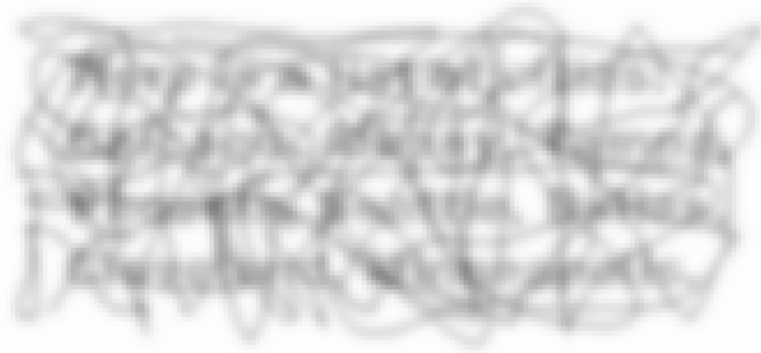
There is a list of states:
Alabama, Ohio, Florida,
Louisiana, Colorado,
Kansas, Mississippi.



A Visual Analogy

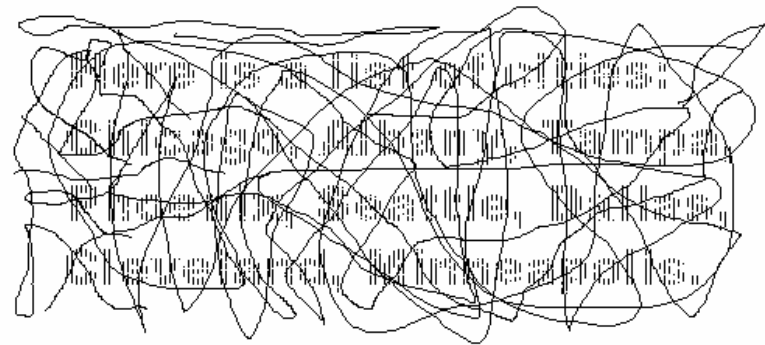
Low-Res OK for single-source

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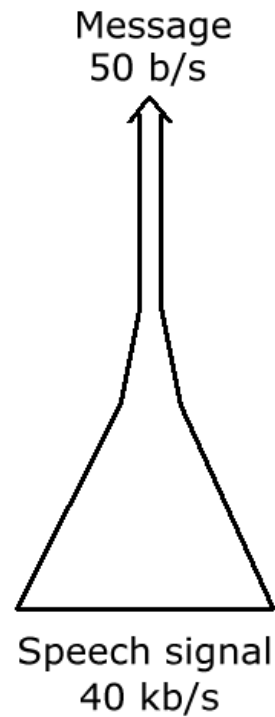
Hi-Res Needed for multi-source

Here is a list of states:
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Kansas, Wisconsin.



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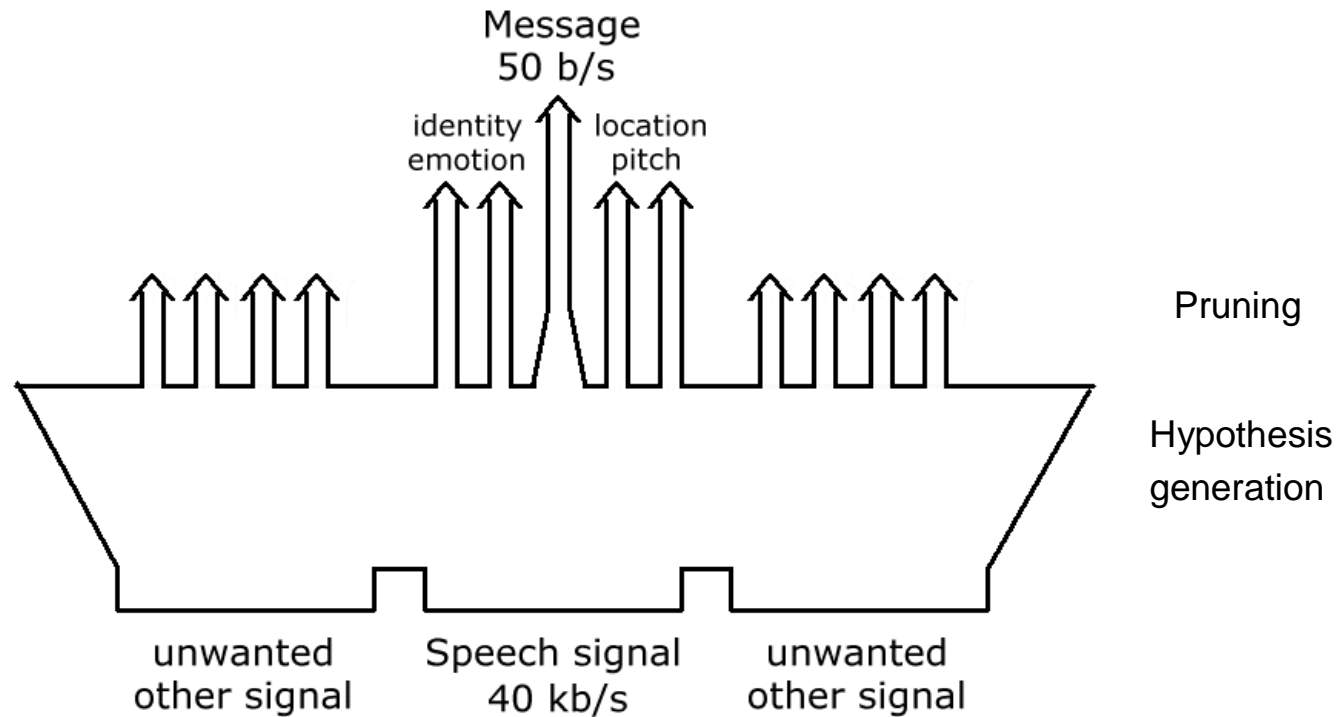
Information Flow for Single-Source



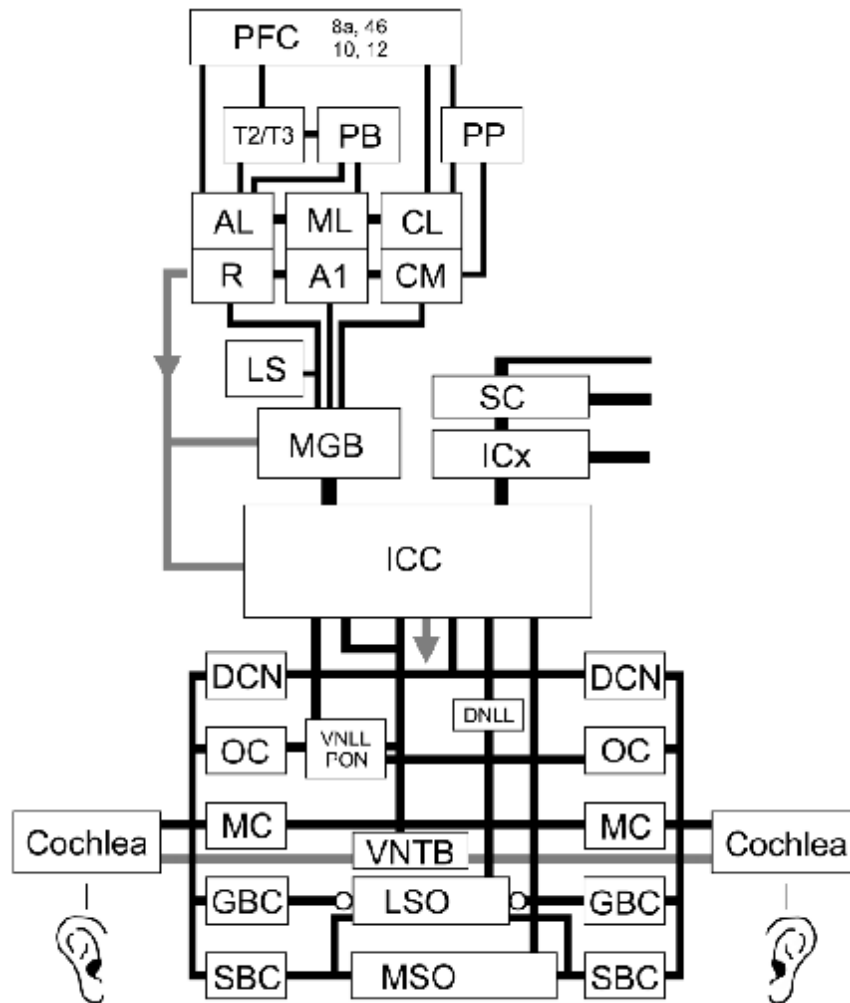
(Hermansky, 1998)

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Information Flow for Multi-Source



Architectural Implications



- Cortical functions: extensive pattern match, hypothesis generation and pruning, object tracking, HMM/Viterbi search, associative memory
- High-res feature detection, cross- and auto-correlation, and post-processing
- High-resolution sensory pre-processing

Architectural Implications

Human-Level Performance on Real-World Multi-Input Sensory Processing will require:

- Further algorithm development to define robust computational pipeline (analogous to graphics rendering pipeline)
- Real-time Hearing will likely require 10-100 GOps range
- Will need efficiencies better than 3 Gops/W for commercial acceptance in many applications
- Priority on fast-turnaround design, high-res visualization, real-time operation, validation on large data sets for robustness, low latency.
- Power consumption, cost reduction are later-stage optimizations once the key operational principles are understood
- Will favor parallel, pipelined, low-clock-rate, low-voltage hardware-accelerated architecture to bring power consumption to reasonable level, but power consumption will trade against chip area and flexibility
- Best implementation model may be GPU – parallel compute for dedicated processing pipeline